

Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number

Filing Date

First Named Inventor

Thomas S. Kobayashi

Group Art Unit

Examiner Name _____

Attorney Docket Number

SC11931TP

11017 U.S. PTO
10/075218
02/14/02

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ²	Kind (if known)			
DF	AA	US6,239,455B1		Becker et al.	May 29, 2001	
DF	AB	5,731,624		Motsiff et al.	Mar. 24, 1998	
DF	AC	5,821,160		Rodriguez et al.	Oct. 13, 1998	
DF	AD	6,021,078		Le et al.	Feb. 1, 2000	
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DF	AH	US6,295,721B1		Tsai	Oct. 2, 2001	
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DF	AN	US6,191,023B1		Chen	Feb. 20, 2001	

[illegible]

Da Hai

5/24/02

¹ Unique citation designation number. ² See Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached

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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	
				Filing Date	
				First Named Inventor	Thomas S. Kobayashi
				Group Art Unit	
Examiner Name					
Sheet	2	of	2	Attorney Docket Number	SC11931TP

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DF	AO	T. Schiml et al., "A 0.13µm CMOS Platform with Cu/ Low-k Interconnects for System On Chip Applications", 2001 IEEE Symposium on VLSI Technology Digest of Technical Papers, published 5/13/01, 2 pgs.	

Examiner Signature	<i>Da Shi</i>	Date Considered	5/24/02
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